



Serial No. 09/204,585

GAU-2784
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Tremblay, Marc; Joy, William
Assignee: Sun Microsystems, Inc.
Title: Local and Global Register Partitioning in VLIW Processor
Serial No.: 09/204,585 Filing Date: December 3, 1998
Examiner: Unknown Group Art Unit: 2784
Docket No.: SP-3288 US

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Newport Beach, California
June 30, 2000

ASSISTANT COMMISSIONER FOR PATENTS
Washington, D. C. 20231

**INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. § 1.97(c) WITH CERTIFICATION
UNDER 37 C.F.R. § 1.97(e)(1)**

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, Applicant(s) wish to call the following documents (copies enclosed) to the attention of the Examiner. The documents were cited in the enclosed International Search Report from **European Patent Office** for the corresponding SP-3288 WO Application No. PCT/US99/28820.

1. U.S. Patent No. 4,980,819 issued to Cushing et al.;
2. U.S. Patent No. 5,530,817 issued to Masubuchi;
3. European Patent Application No. 0 588 341;
4. European Patent Application No. 0 676 691; and
5. European Patent Application No. 0 767 425.

A PTO form 1449 listing these documents is enclosed.

Citation of the above documents shall not be construed as:

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1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made, other than as described above; or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

The undersigned hereby certifies in accordance with § 1.97(e)(1) that each item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this information disclosure statement.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on June 30, 2000.

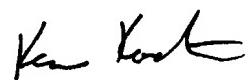
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6/30/00

Date of Signature

Respectfully submitted,



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